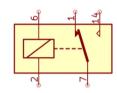
# 5 TIPS TO DESIGN BEILER



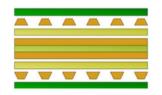
## AVOID BAD SCHEMATIC SYMBOLS

Ready-to-use
symbols can often
be messy and make
your schematic
difficult to read. A
clear and wellorganized
schematic is a
reflection of your
expertise. Consider
creating your own
custom symbols to
enhance clarity and
understanding!



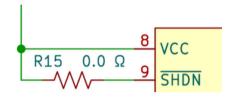
## **ANNOTATE**

Enhance your schematic with plenty of annotations, equations, and images. These additions not only help others understand your design more effectively but also make debugging much easier for you.



## STACKUP AND THEN LAYOUT

Define your stackup
before starting the
layout. It has a
dramatic impact on
critical factors such
as trace widths
(current handling and
manufacturability),
minimum trace
spacing, via sizes, and
impedance matching
quidelines



### **JUMPERS IS KEY**

For prototypes, include  $0\Omega$  resistors wherever possible, especially in circuits where time constraints limited your design process.



#### **GROUND TEST POINTS**

Distribute ground test points across different locations to ensure easy access during debugging.

